

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	261993	(stor\$4 or memor\$4 or record\$3 or registor\$3 or databas\$3)same(segment\$6 or divid\$3 or partition\$3 or partial\$3 or portion\$3 or region\$3 or extract\$3 or block\$3)same(synthes\$4 or merg\$3 or combin\$6 or compos\$6 or mosiac\$4 or mesh\$3 or superimpos\$4)	US-PGPUB; USPAT	OR	ON	2007/04/24 18:23
L2	49413	1 same(address\$3 or cod\$3 )	US-PGPUB; USPAT	OR	ON	2007/04/24 18:24
L3	543	2 same(Position\$3 near10 imag\$3)	US-PGPUB; USPAT	OR	ON	2007/04/24 18:25
L4	1222	2 same(synchroniz\$4)	US-PGPUB; USPAT	OR	ON	2007/04/24 18:25
L5	25	4 same(Position\$3 near10 imag\$3)	US-PGPUB; USPAT	OR	ON	2007/04/24 18:13
L6	468	2 same(parallel near10 process\$4)	US-PGPUB; USPAT	OR	ON	2007/04/24 18:08
L7	6	6 same(Position\$3 near10 imag\$3)	US-PGPUB; USPAT	OR	ON	2007/04/24 18:25
L8	36728	(imag\$3)same(stor\$4 or memor\$4 or record\$3 or registor\$3 or databas\$3)same(segment\$6 or divid\$3 or partition\$3 or partial\$3 or portion\$3 or region\$3 or extract\$3 or block\$3)same(synthes\$4 or merg\$3 or combin\$6 or compos\$6 or mosiac\$4 or mesh\$3 or superimpos\$4)	US-PGPUB; USPAT	OR	ON	2007/04/24 18:23
L9	6088	8 same(address\$3 or cod\$3 )	US-PGPUB; USPAT	OR	ON	2007/04/24 18:24
L10	543	9 same(Position\$3 near10 imag\$3)	US-PGPUB; USPAT	OR	ON	2007/04/24 18:25
L11	25	10 same(synchroniz\$4)	US-PGPUB; USPAT	OR	ON	2007/04/24 18:25
L12	543	10 same(Position\$3 near10 imag\$3)	US-PGPUB; USPAT	OR	ON	2007/04/24 18:25
L13	1	"6654018".PN.	USPAT; USOCR	OR	ON	2007/04/24 18:26
L14	1	"6611613".PN.	USPAT; USOCR	OR	ON	2007/04/24 18:26
L15	1	"6504546".PN.	USPAT; USOCR	OR	ON	2007/04/24 18:26
L16	1	"6054999".PN.	USPAT; USOCR	OR	ON	2007/04/24 18:26

## EAST Search History

L17	1	"5933151".PN.	USPAT; USOCR	OR	ON	2007/04/24 18:27
L18	1	"5815392".PN.	USPAT; USOCR	OR	ON	2007/04/24 18:27
L19	1	"5815392".PN.	USPAT; USOCR	OR	ON	2007/04/24 18:27
L20	1	"6434278".PN.	USPAT; USOCR	OR	ON	2007/04/24 18:27
L21	1	"6377269".PN.	USPAT; USOCR	OR	ON	2007/04/24 18:27
L22	1	"6351572".PN.	USPAT; USOCR	OR	ON	2007/04/24 18:28
L23	1	"5991437".PN.	USPAT; USOCR	OR	ON	2007/04/24 18:28
L24	1	"5187521".PN.	USPAT; USOCR	OR	ON	2007/04/24 18:48
L25	1	"6674427".PN.	USPAT; USOCR	OR	ON	2007/04/24 19:20
L26	1	"6456326".PN.	USPAT; USOCR	OR	ON	2007/04/24 19:20

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**Key:** IEEE JNL = IEEE Journal or Magazine, IEE JNL = IEE Journal or Magazine, IEEE CNF = IEEE Conference, II CNF = IEE Conference, IEEE STD = IEEE Standard

1. **Synthesis of heterogeneous distributed architectures for memory-intensive applications**  
Chao Huang; Ravi, S.; Raghunathan, A.; Jha, N.K.;  
Computer Aided Design, 2003. ICCAD-2003. International Conference on  
9-13 Nov. 2003 Page(s):46 - 53  
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2. **Non-segmental analysis and synthesis based on a speech database**  
Slater, A.; Coleman, J.;  
Spoken Language, 1996. ICSLP 96. Proceedings., Fourth International Conference on  
Volume 4, 3-6 Oct. 1996 Page(s):2379 - 2382 vol.4  
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3. **Co-RAM: combinational logic synthesis applied to software partitions for mapping to a novel memory device**  
Weber, S.J.; Paul, J.M.; Thomas, D.E.;  
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4. **An efficient multiprocessor merge algorithm**  
Varman, P.J.; Iyer, B.R.; Haderle, D.J.;  
Databases, Parallel Architectures and Their Applications., PARBASE-90, International Conference on  
7-9 March 1990 Page(s):276 - 283  
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5. **Hardware/software co-synthesis with memory hierarchies**  
Yanbing Li; Wolf, W.H.;  
Computer-Aided Design of Integrated Circuits and Systems, IEEE Transactions on  
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6. **New image retrieval paradigm: logical composition of region categories**  
Fauqueur, J.; Boujemaa, N.;  
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Volume 3, 14-17 Sept. 2003 Page(s):III - 601-4 vol.2  
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7. **Exchange coupled composite media for perpendicular magnetic recording**  
Jian-Ping Wang; Weikang Shen; Bai, J.;  
Magnetics, IEEE Transactions on  
Volume 41, Issue 10, Oct. 2005 Page(s):3181 - 3186  
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8. **Composite media for perpendicular magnetic recording**  
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9. **Czech speech synthesizer Popokatepetl based on word corpus**  
Gaura, P.;  
Video/Image Processing and Multimedia Communications, 2003. 4th EURASIP Conference focused on  
Volume 2, 2-5 July 2003 Page(s):673 - 678 vol.2  
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10. **A framework for parallelizing load/stores on embedded processors**  
Xiaotong Zhuang; Pande, S.; Greenland, J.S.;  
Parallel Architectures and Compilation Techniques, 2002. Proceedings. 2002 International Conference on  
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11. **An auditory-based measure for improved phone segment concatenation**  
Chappell, D.T.; Hansen, J.H.L.;  
Acoustics, Speech, and Signal Processing, 1997. ICASSP-97., 1997 IEEE International Conference on  
Volume 3, 21-24 April 1997 Page(s):1639 - 1642 vol.3  
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12. **Building a speech database for the purpose of speaker specific speech synthesis**  
Hoory, R.; Shaked, N.; Chazan, D.;  
Signal Processing, 1996., 3rd International Conference on  
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13. **A phonetically labeled acoustic segment (PLAS) approach to speech analysis-synthesis**  
Soong, F.K.;  
Acoustics, Speech, and Signal Processing, 1989. ICASSP-89., 1989 International Conference on  
23-26 May 1989 Page(s):584 - 587 vol.1  
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14. **Vectorial Boolean functions and induced algebraic equations**  
Golic, J.Dj.;  
Information Theory, IEEE Transactions on  
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15. **Magnetic microstructures produced by compositional separation in Co-Cr based alloy thin films**  
Maeda, Y.; Rogers, D.J.; Ohsung Song; Takei, K.; Ohkubo, T.; Hirono, S.; Suzuki, J.; Mori, Y.;  
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16. **Object segmentation and labeling by learning from examples**  
Yaowu Xu; Saber, E.; Tekalp, A.M.;  
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Volume 12, Issue 6, June 2003 Page(s):627 - 638  
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17. **Synthesis tool for low-power finite-state machines with mixed synchronous/asynchronous state memory**  
Cao, C.; O'Nils, M.; Oelmann, B.;  
Computers and Digital Techniques, IEE Proceedings-  
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18. **Segmented-Region Based Approach Using Object Components Database to Detect Sky Region**  
Takahashi, Y.; Hirata, K.;  
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- 19. Mining Association Rules from Data with Missing Values by Database Partitioning and Merging**  
Shintani, T.;  
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- 20. Storage assignment during high-level synthesis for configurable architectures**  
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- 21. Detecting irregularities in images and in video**  
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- 22. LURU: global-scope FPGA technology mapping with content-addressable memories**  
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- 23. Automatic synthesis of customized local memories for multicluster application accelerators**  
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Application-Specific Systems, Architectures and Processors, 2004. Proceedings. 15th IEEE International Conferenc  
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- 24. Memory conscious task partition and scheduling in grid environments**  
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- 25. A unit selection approach to F0 modeling and its application to emphasis**  
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30 Nov.-3 Dec. 2003 Page(s):700 - 705  
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